

FIG. 3A

(CORE AREA) (PERIPHERAL CIRCUIT AREA)

16 15 14 13 17a 13 17b 13

FIG. 3B

(CORE AREA) (PERIPHERAL CIRCUIT AREA)

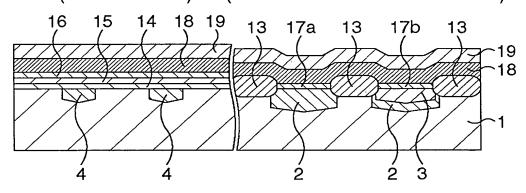


FIG. 3C

(CORE AREA) (PERIPHERAL CIRCUIT AREA)

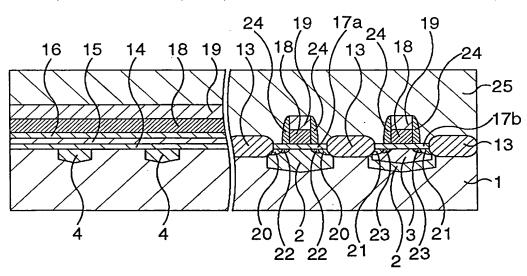


FIG. 4A

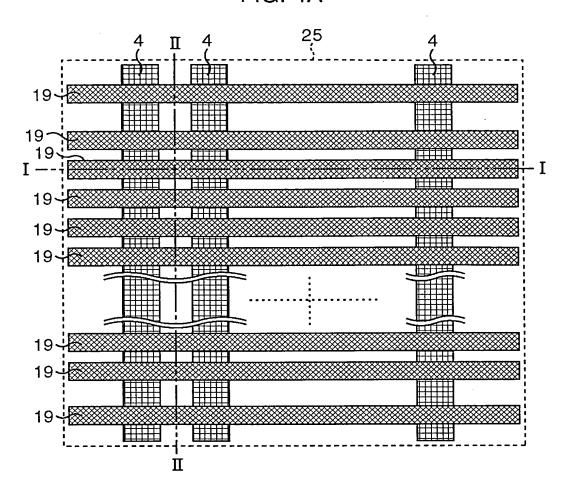


FIG. 4B

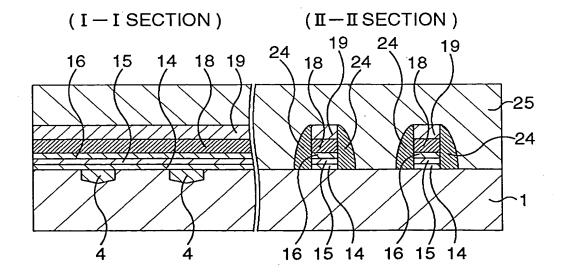


FIG. 5A

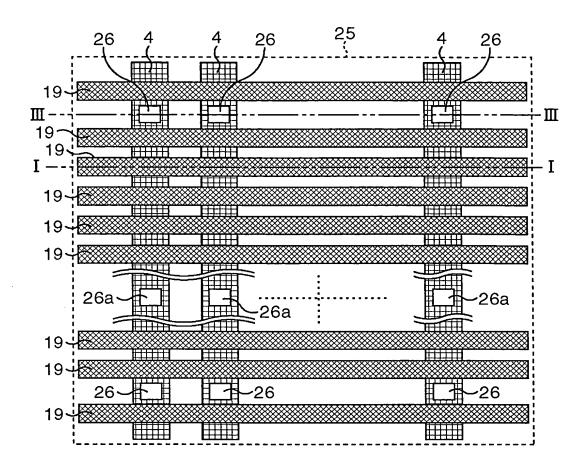


FIG. 5B

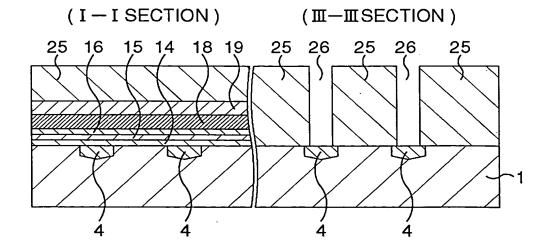


FIG. 6A

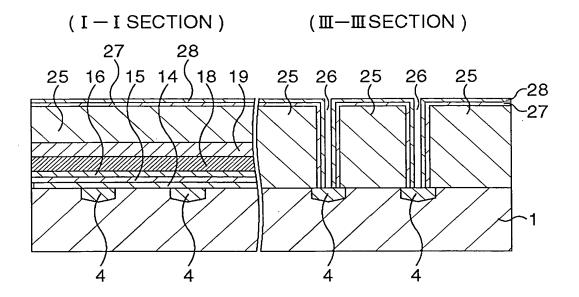
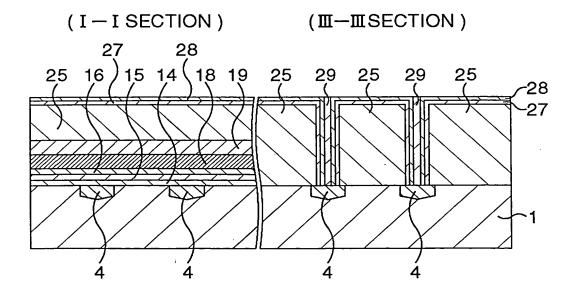
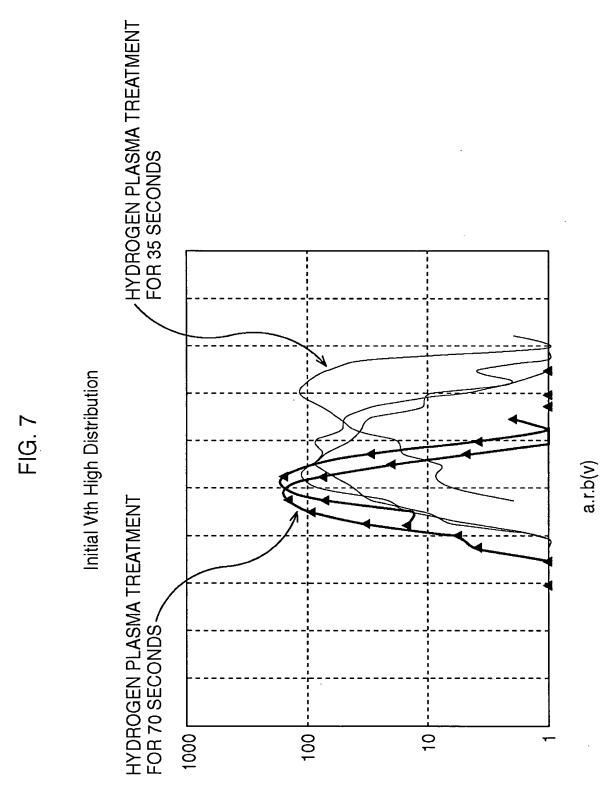


FIG. 6B





NUMBER OF MEASURED SEMICONDUCTOR MEMORY DEVICES(FREQUENCY)